

FIG. 1

200A

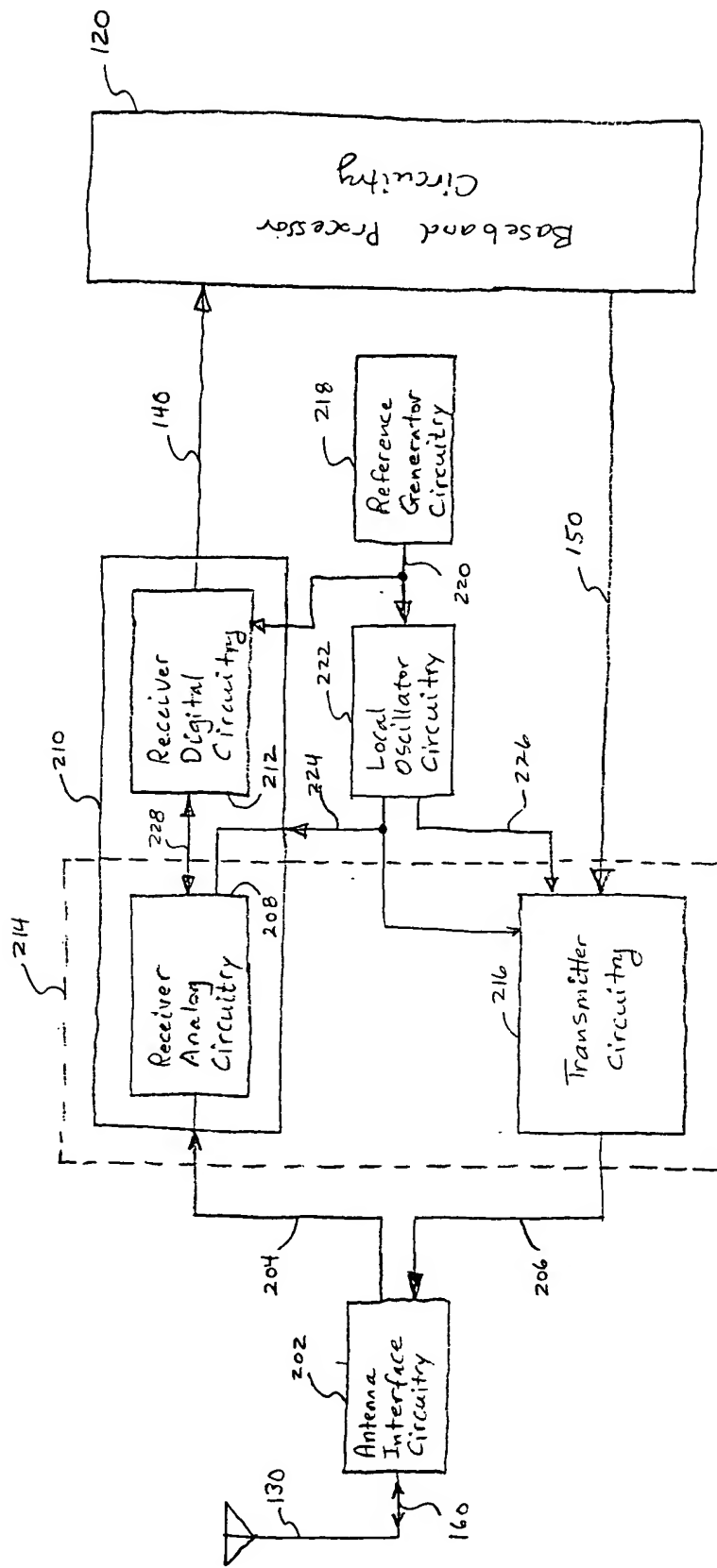


FIG. 2A

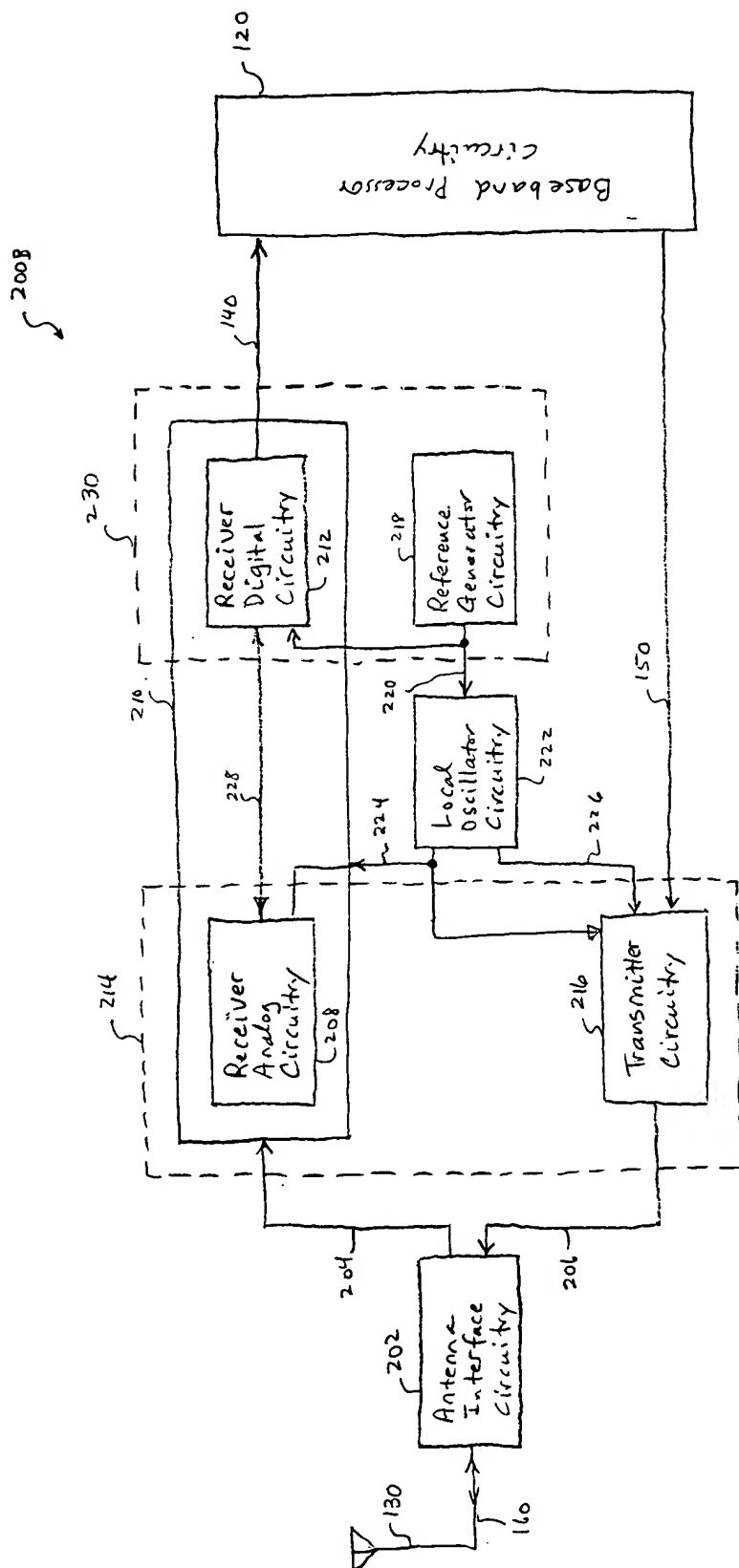


FIG. 2B

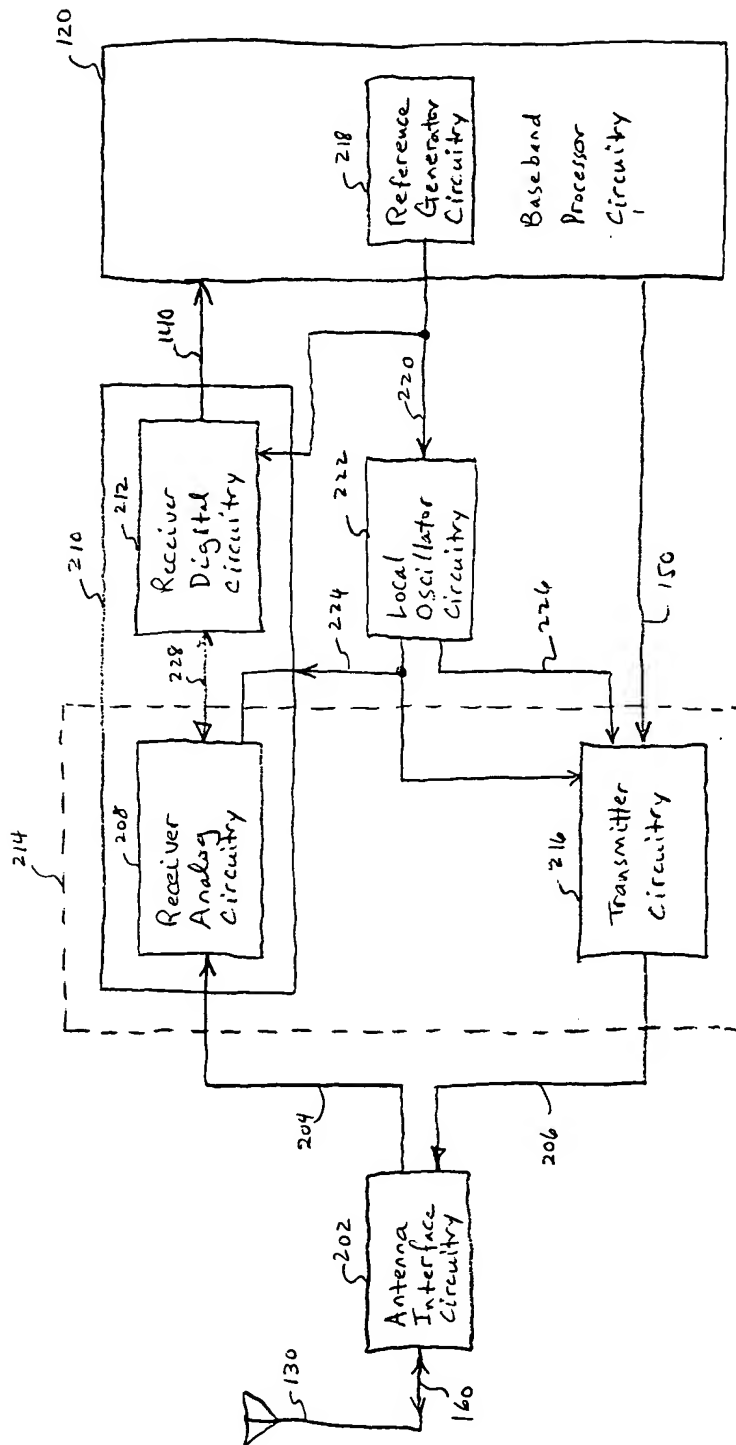


FIG. 2C

200D

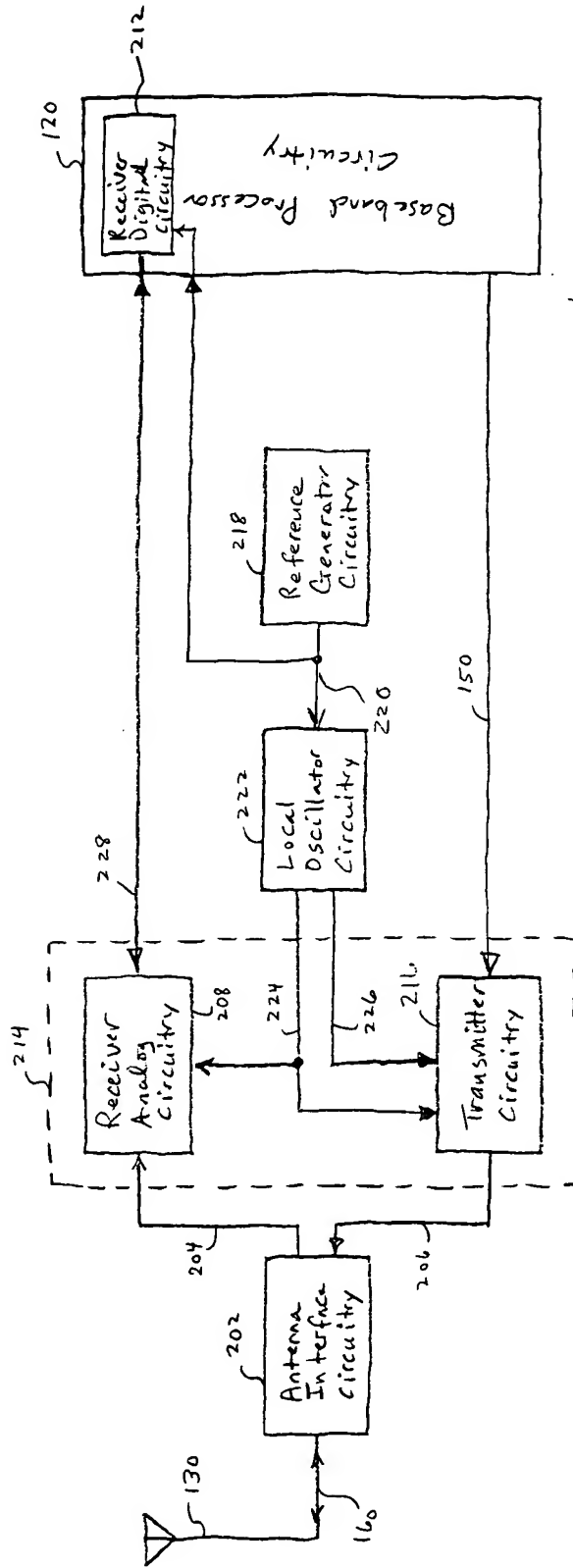


FIG. 2D

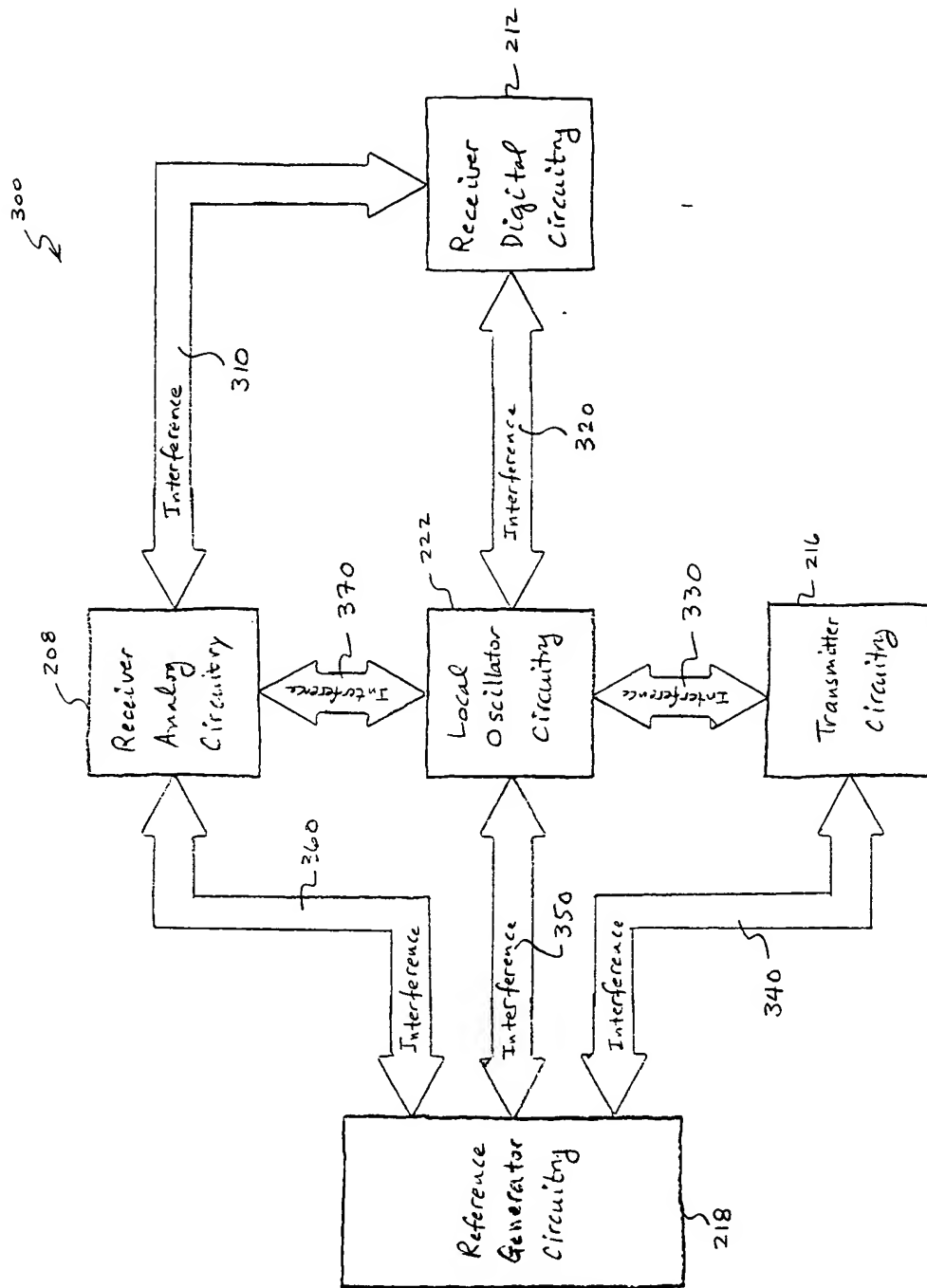


FIG. 3

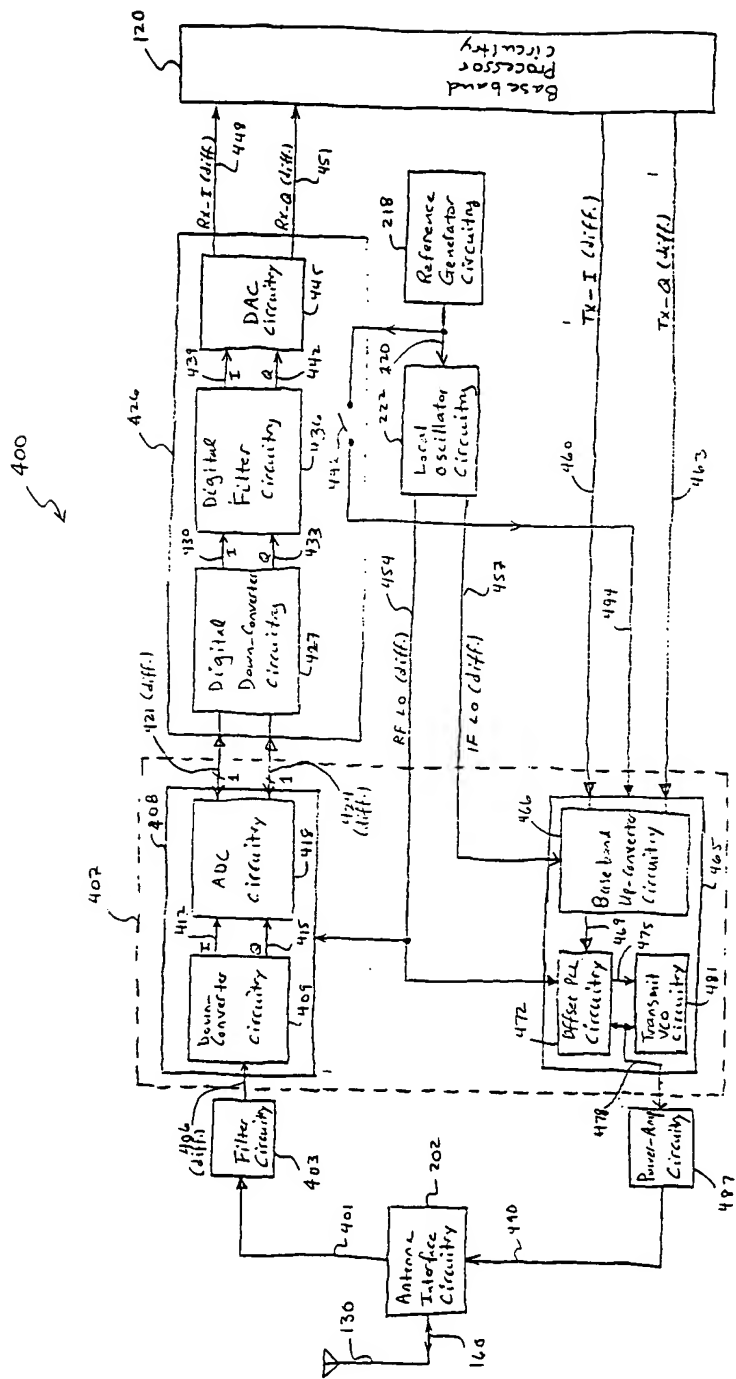


FIG. 4

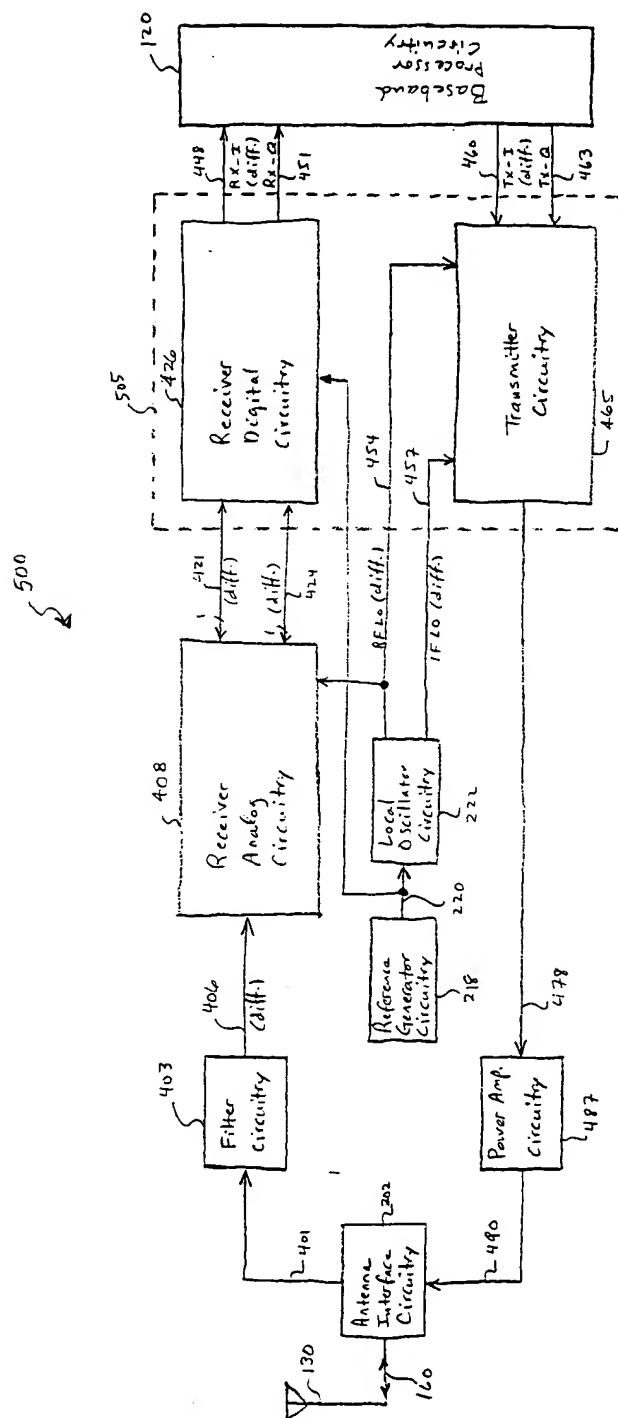


FIG. 5



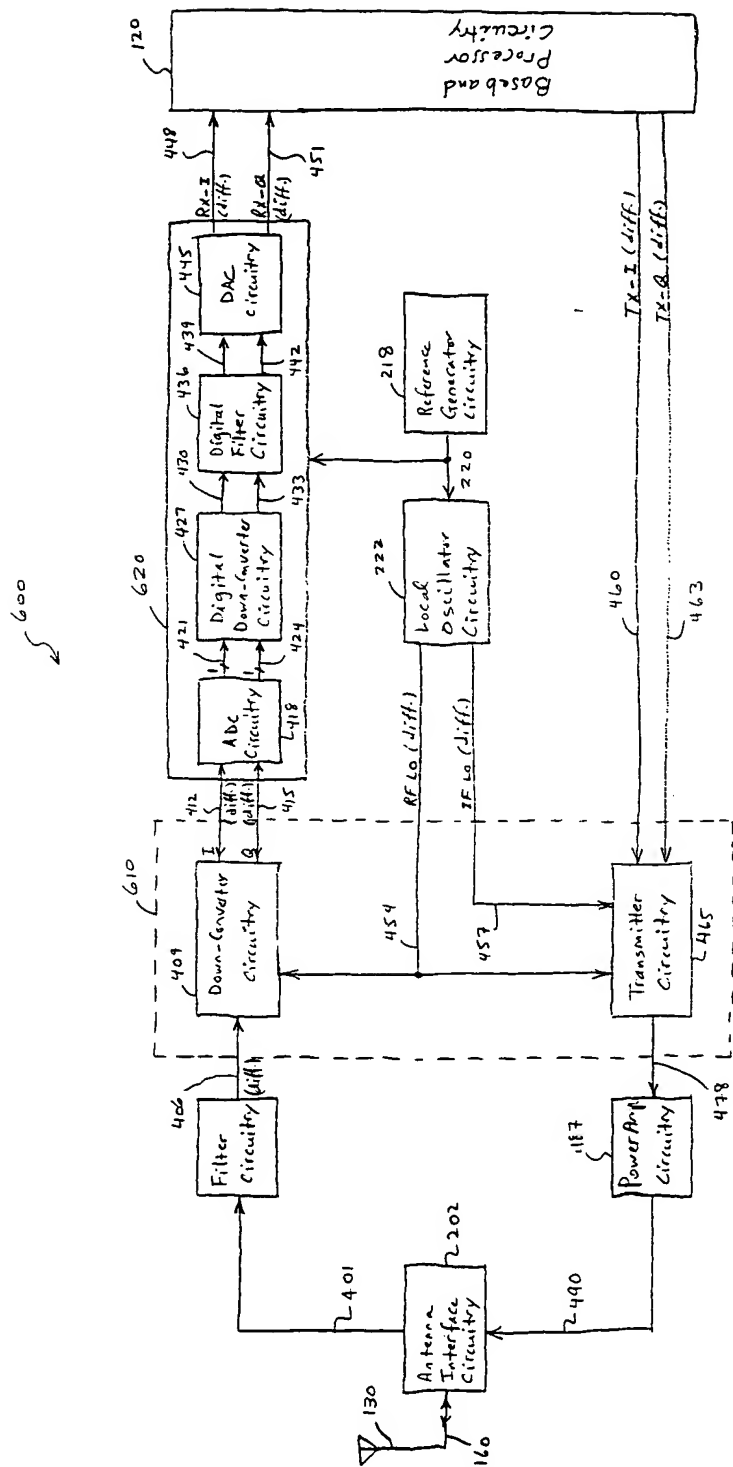


FIG. 6

700

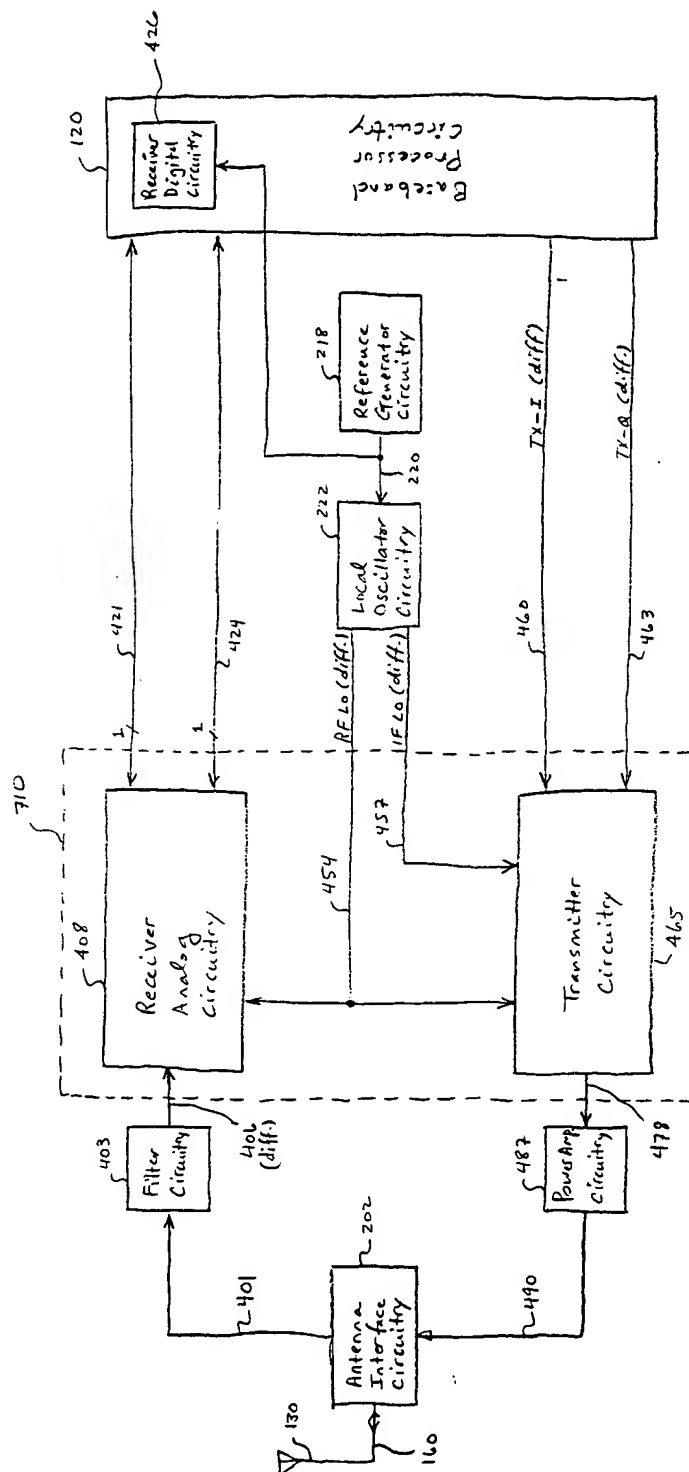


FIG. 7

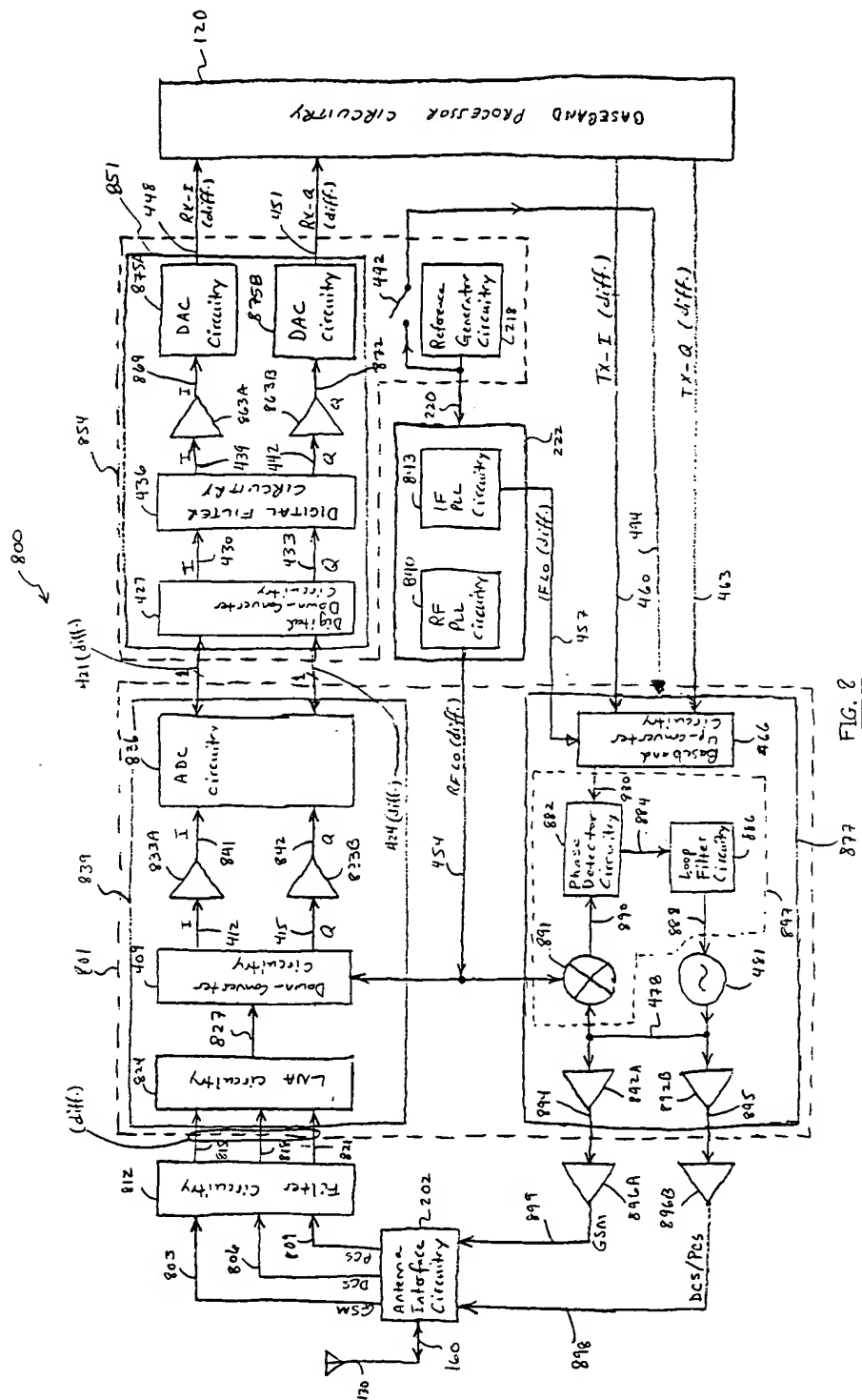


FIG. 2

910A

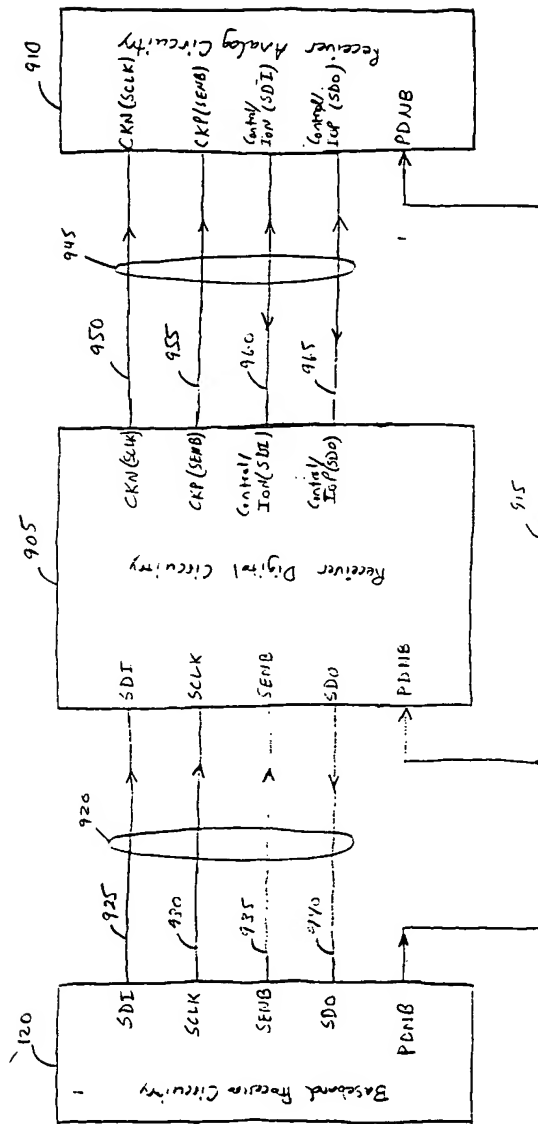


FIG. 9A

900B

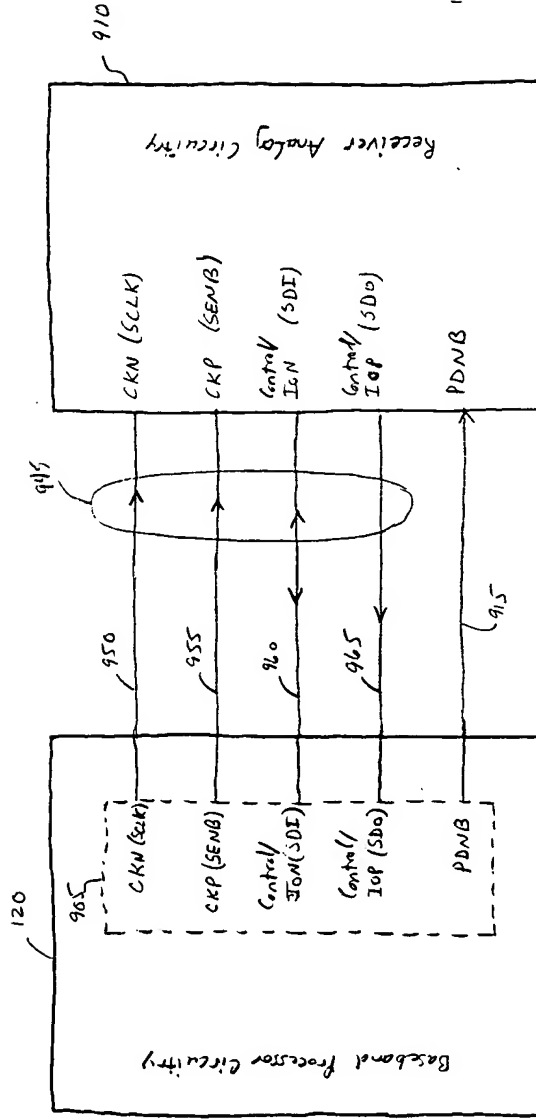


FIG. 9B

FIG. 10 is a block diagram of a receiver circuitry 1000, which includes a Baseband Receiver Circuitry 120, a Receiver Digital Circuitry 905, and a Receiver Analog Circuitry 910.

1000

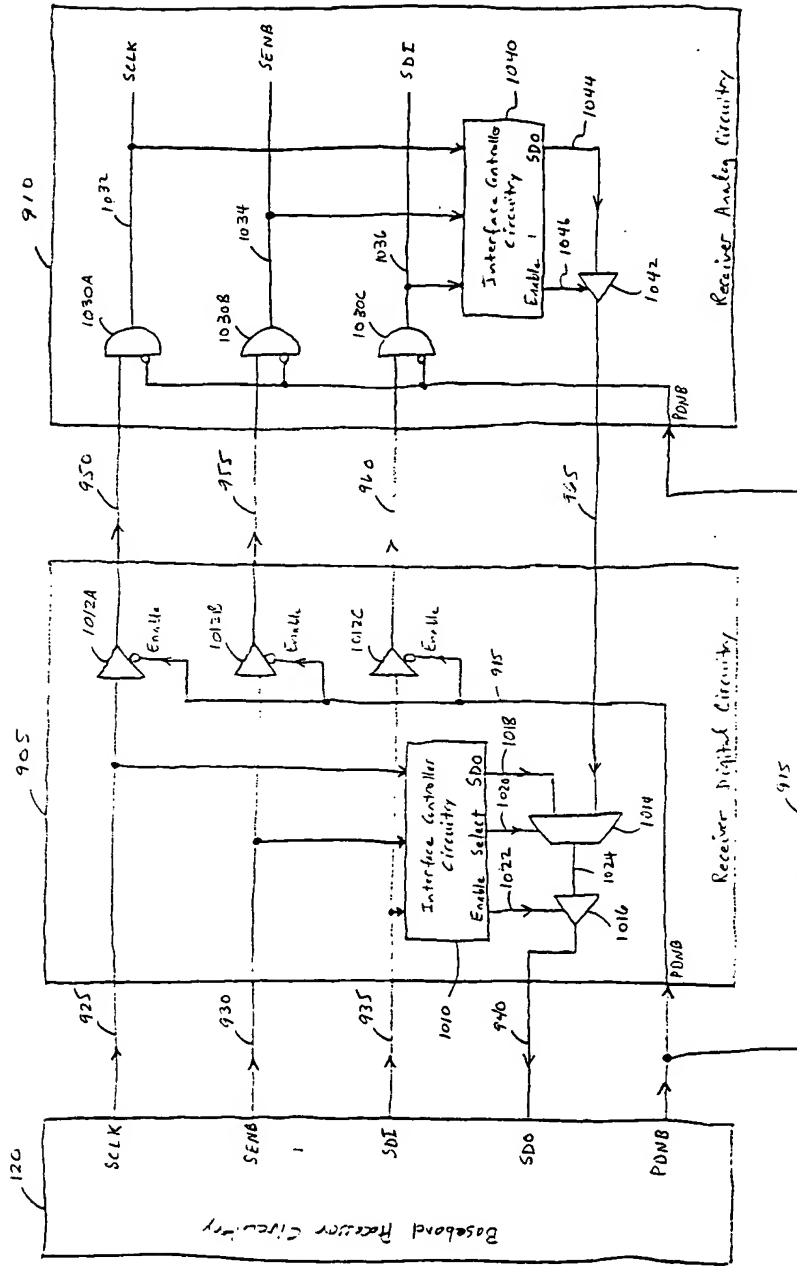


FIG. 10

FIG. 11A is a block diagram of a receiver system 1100A. The system includes a Reference Signal 220, a Receiver Digital Circuitry 905, and a Receiver Analog Circuitry 910. The Receiver Digital Circuitry 905 contains a Signal Processing Circuitry 1110, an Interface Controller Circuitry 1116, a Delay-Cell Circuitry 1119, and two D-type flip-flops 1123A and 1123B. The Receiver Analog Circuitry 910 contains an ADC 1142, an Interface Controller Circuitry 1140, and a Comparator 1154. The system is controlled by a Clock for adjustment/calibration circuitry 1130 and a Complement clock signal (may also be used as clock signal) 1132A. The system also includes an Enable signal 1142 and a Clock signal 1130. The system is labeled 1100A.

1100A

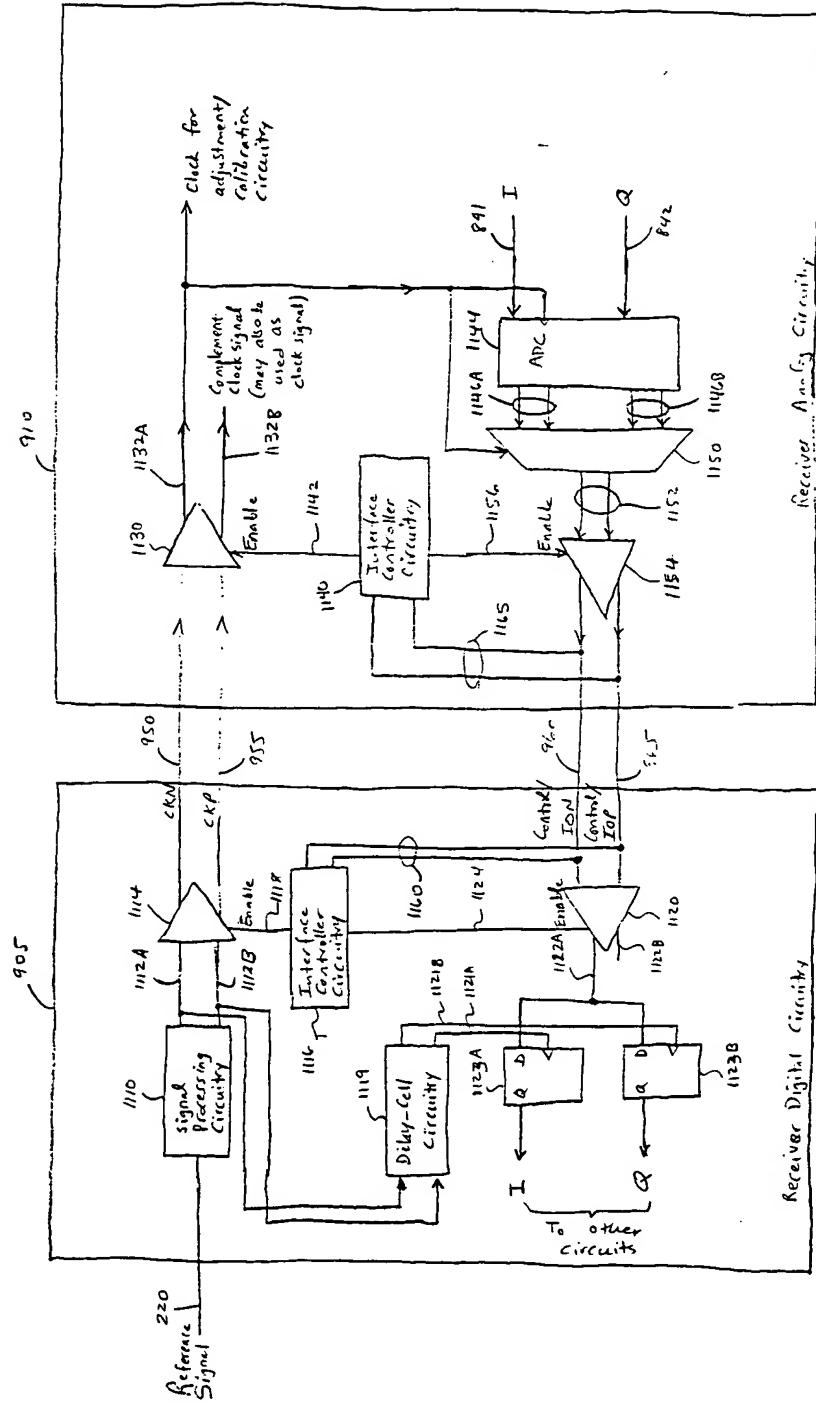


FIG. 11A

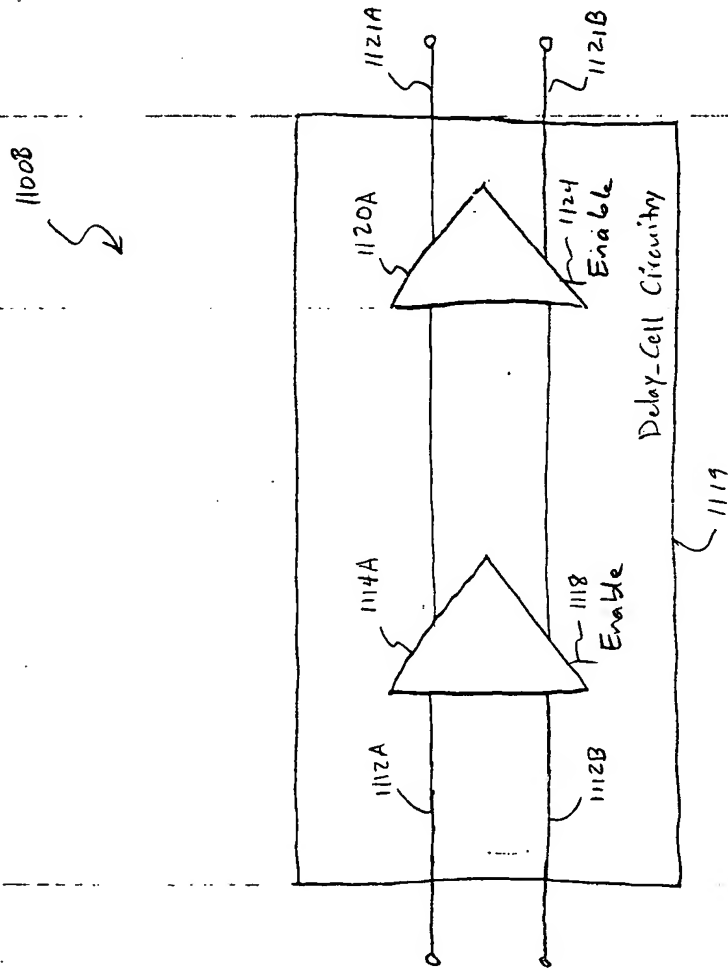


FIG. 11B



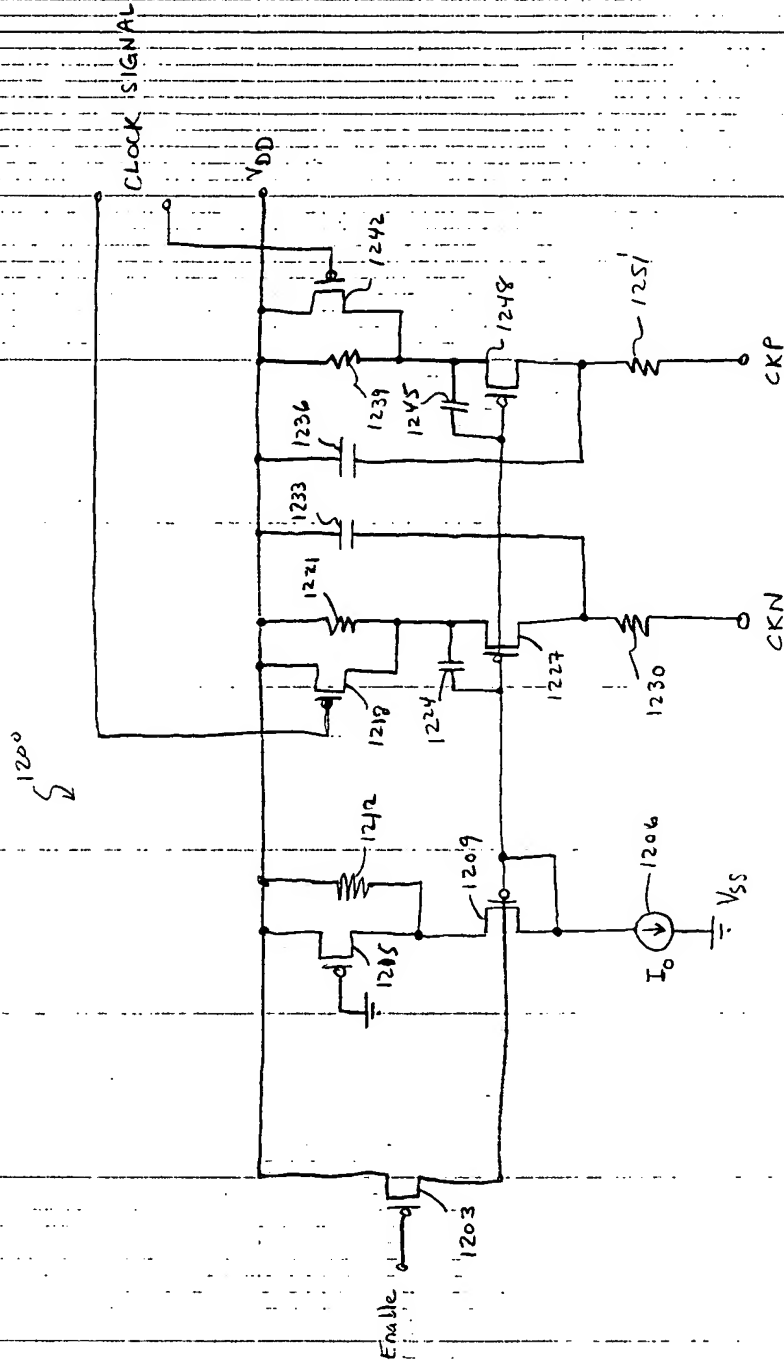
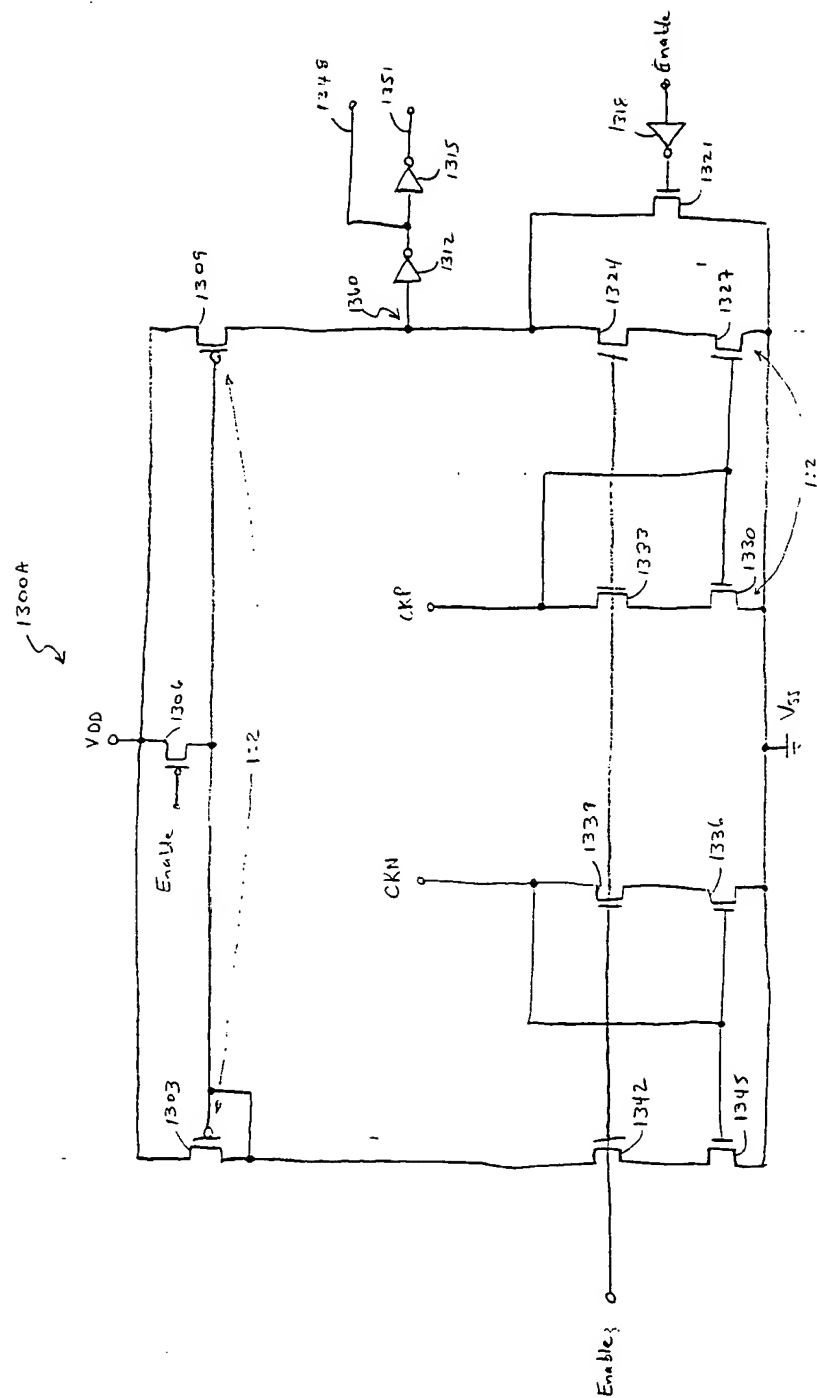


FIG. 12



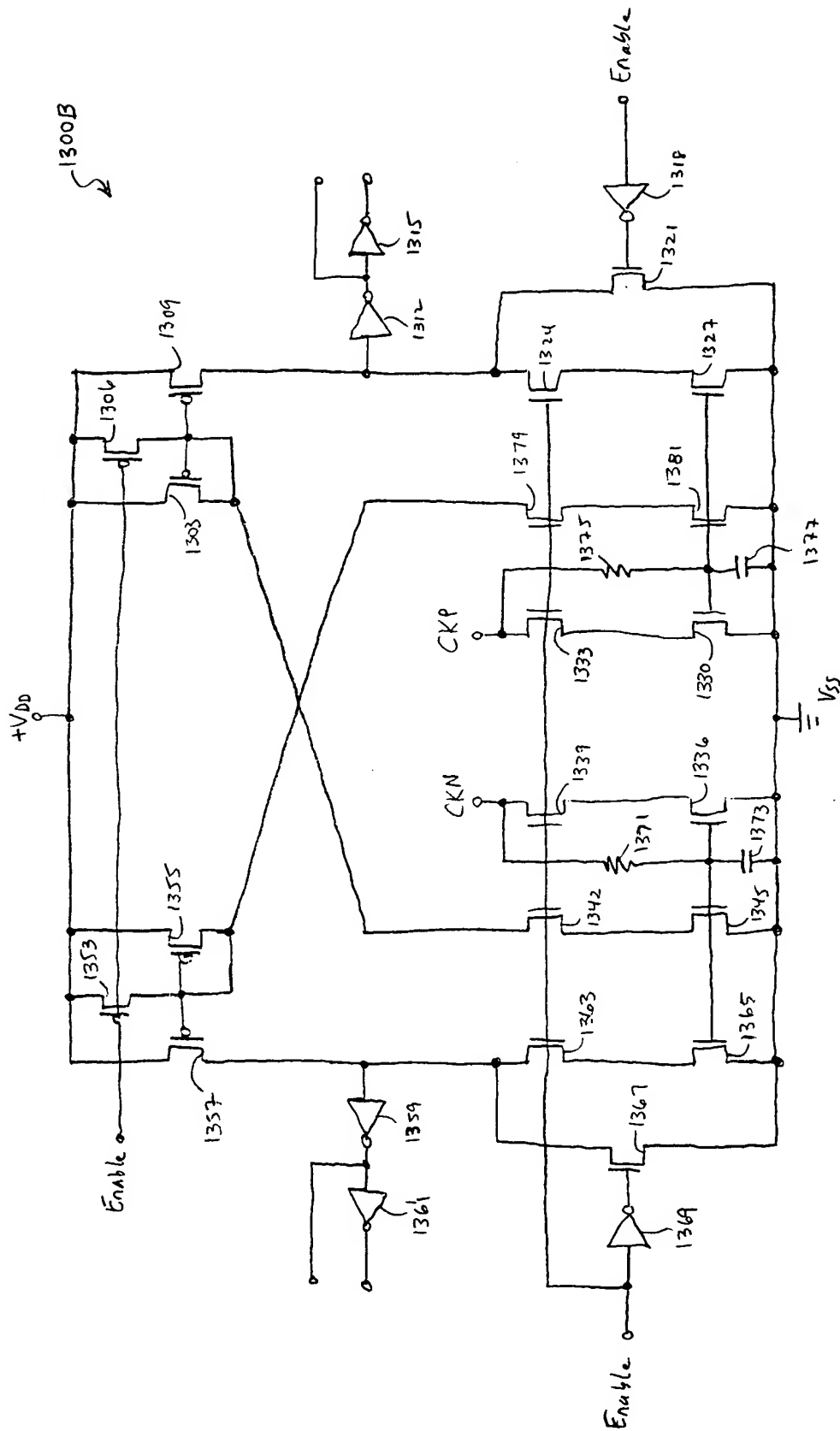


FIG. 13B

1400

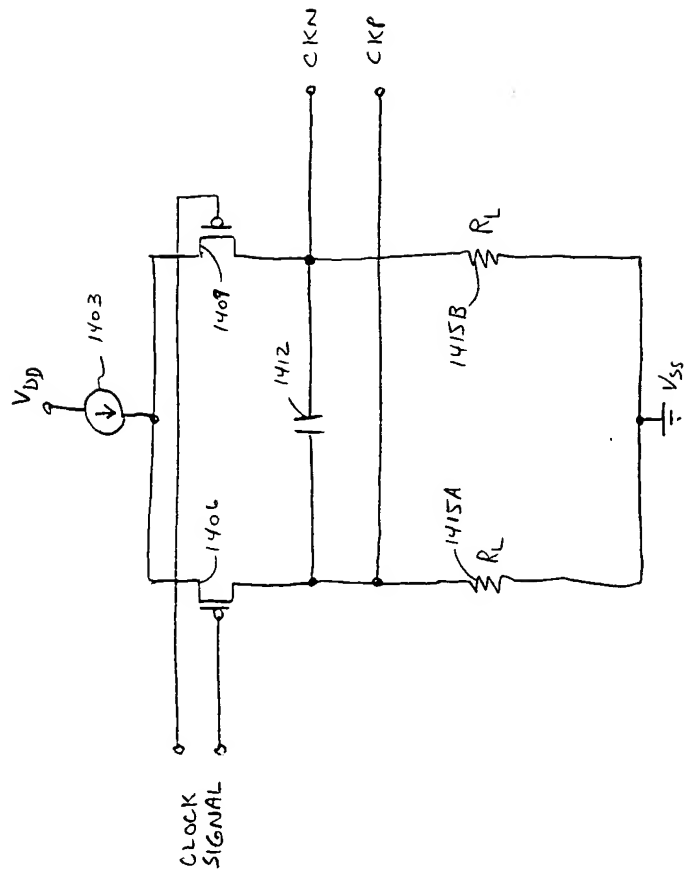


FIG. 14

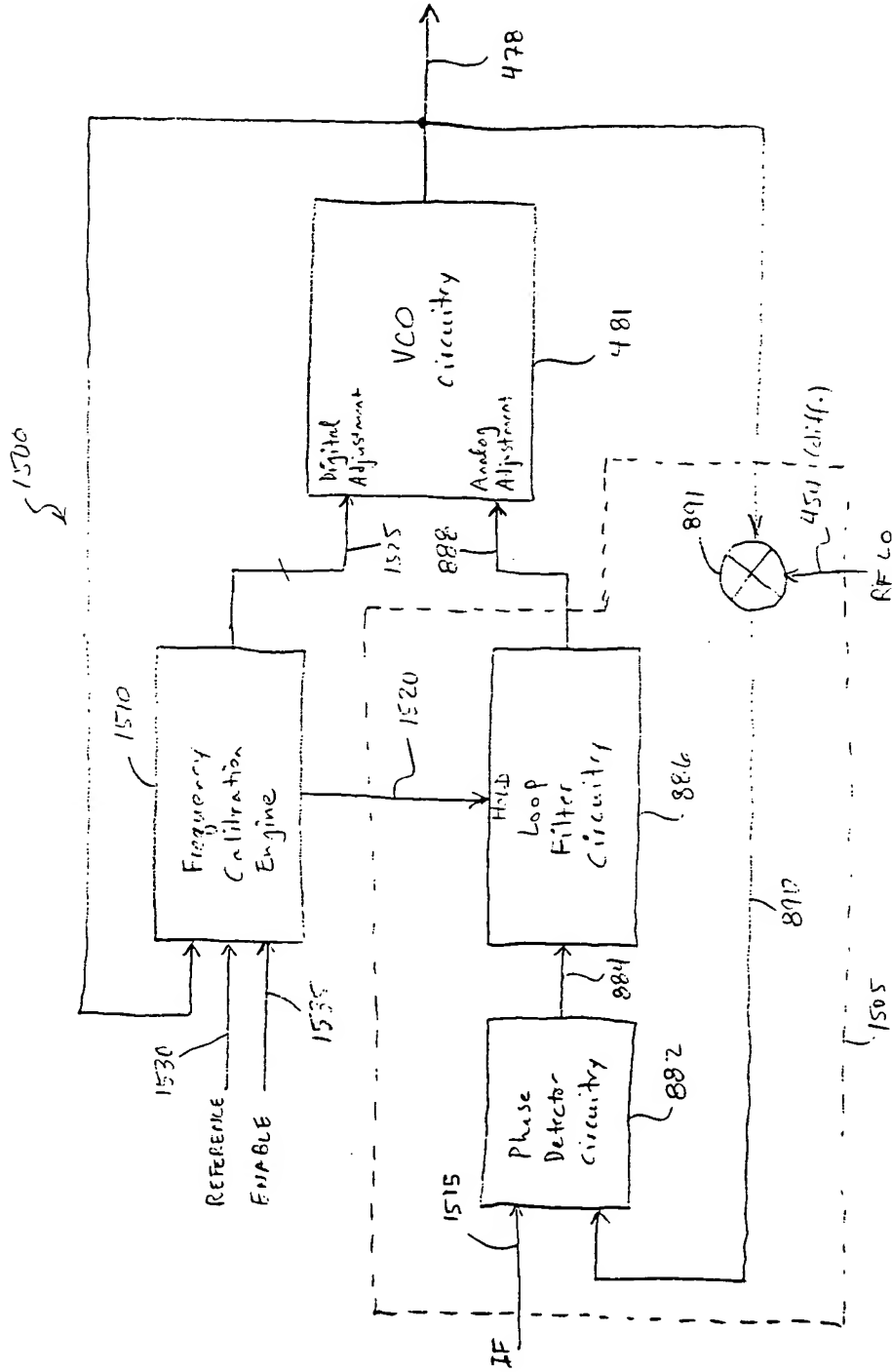


FIG. 15

121

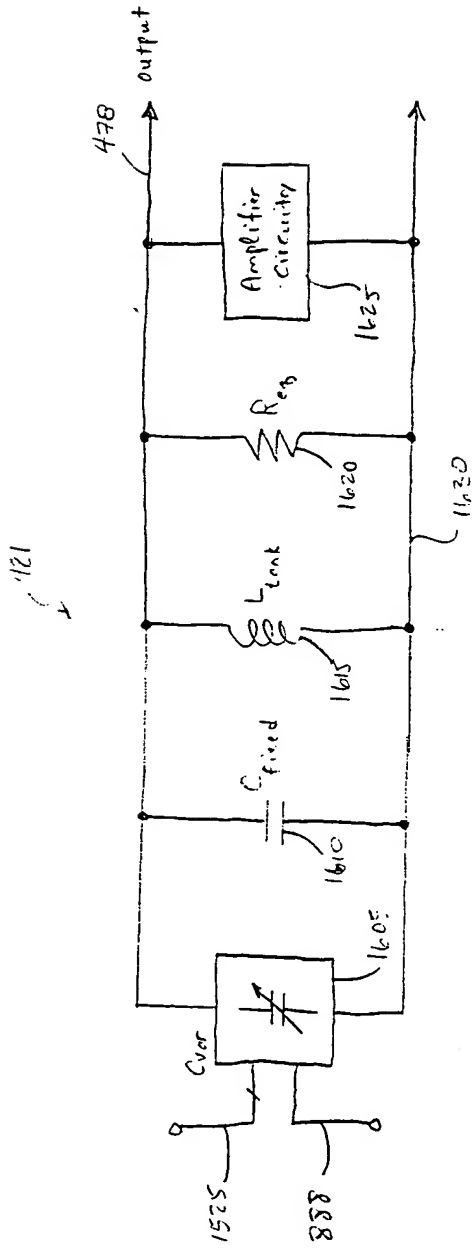


FIG. 16

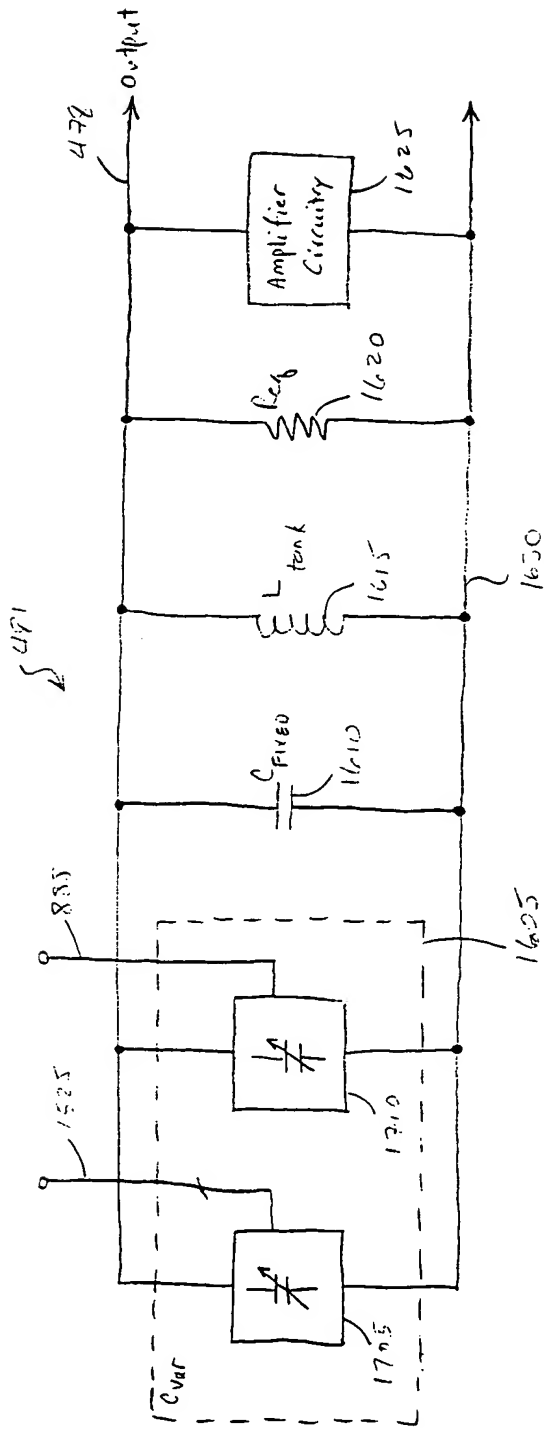


FIG. 17

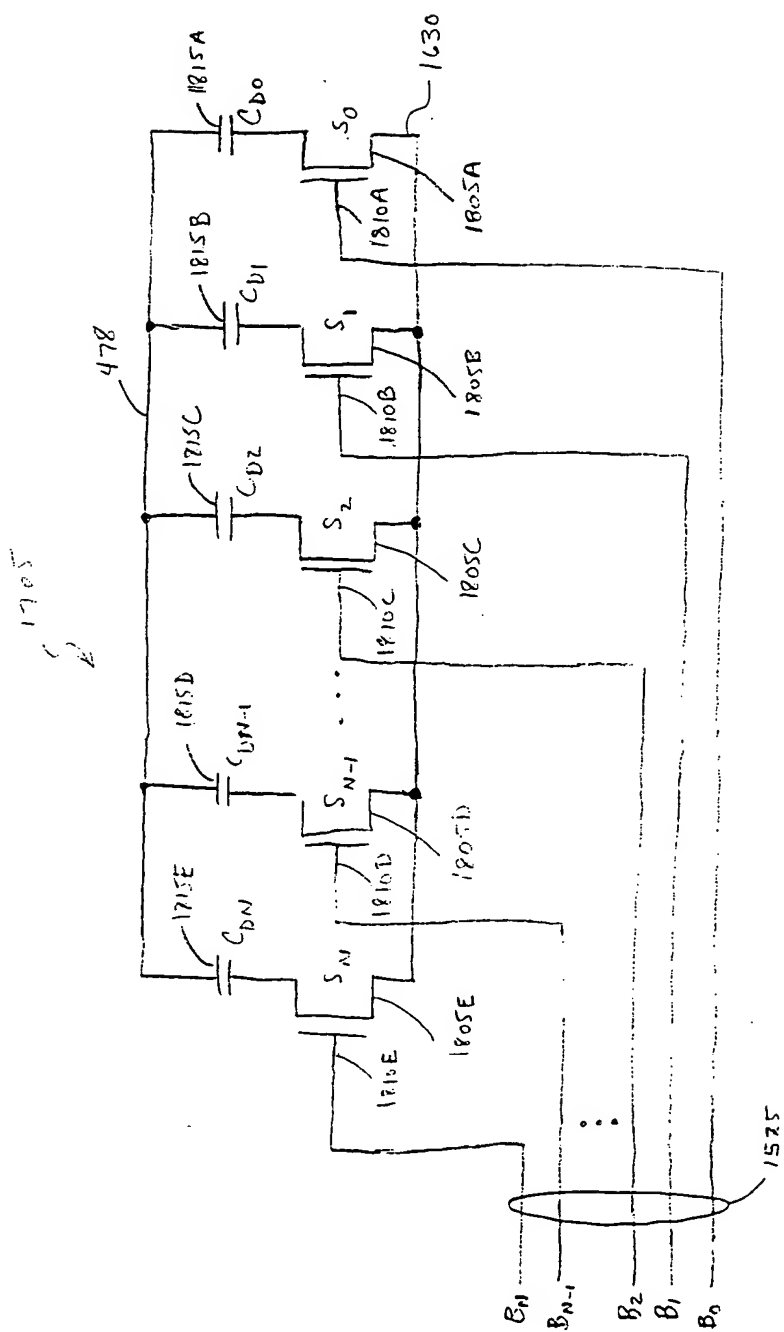
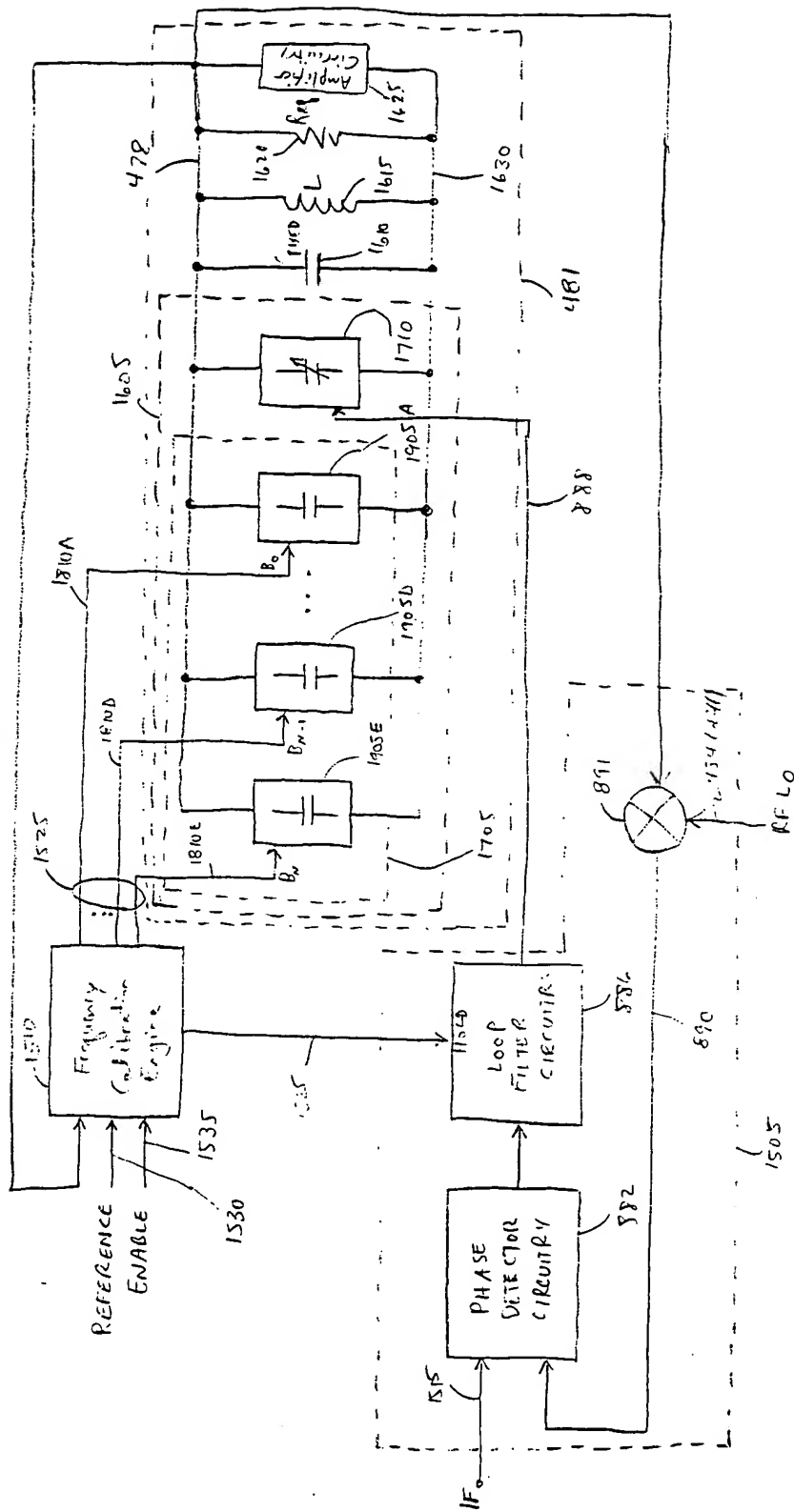


FIG. 18



1900A





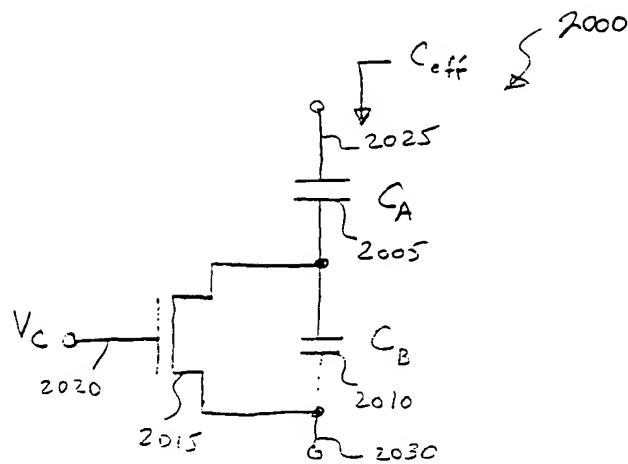


FIG. 20

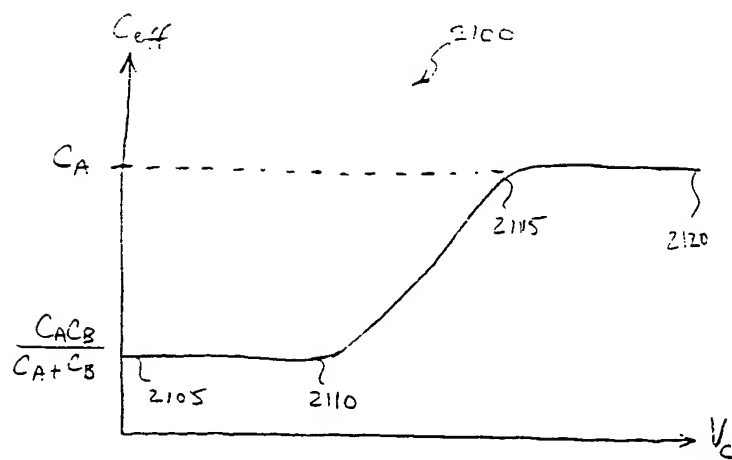


FIG. 21

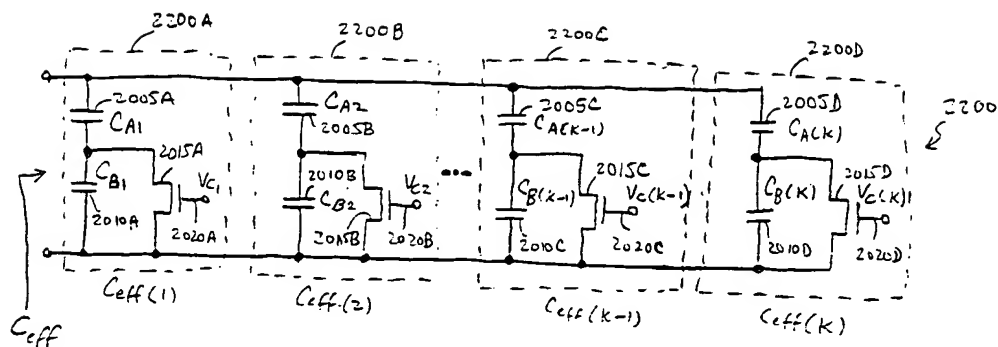


FIG. 22

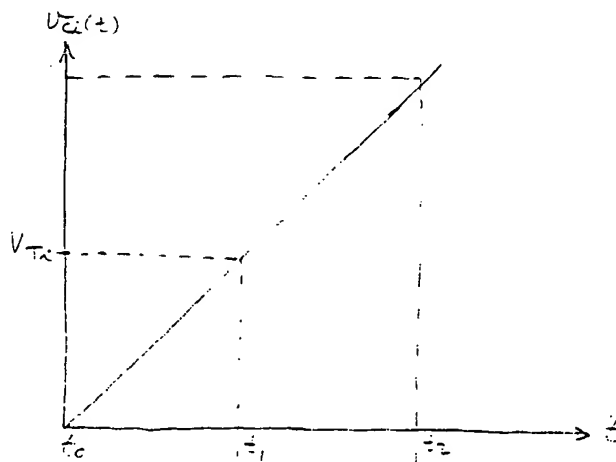


FIG. 23A

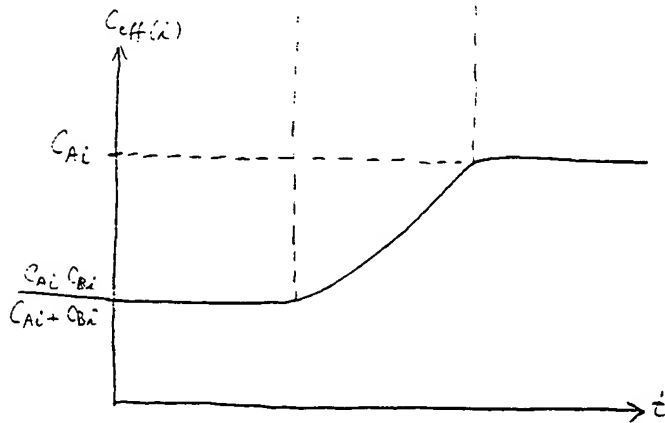


FIG. 23B

FIG. 24A

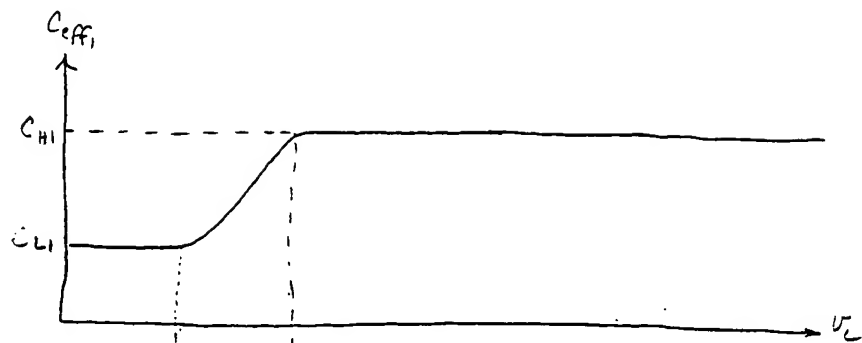


FIG. 24B

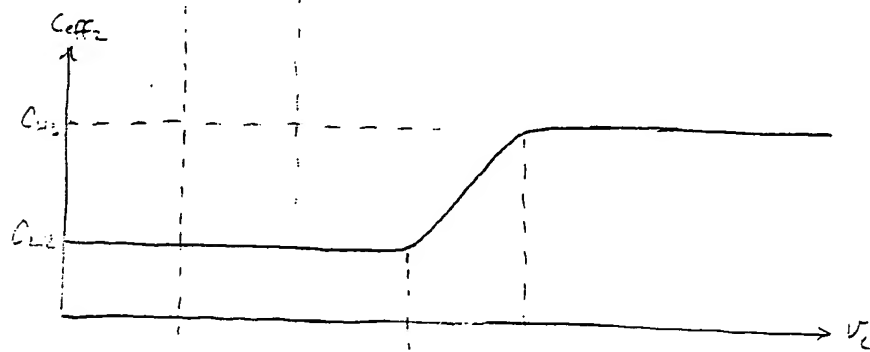


FIG. 24C

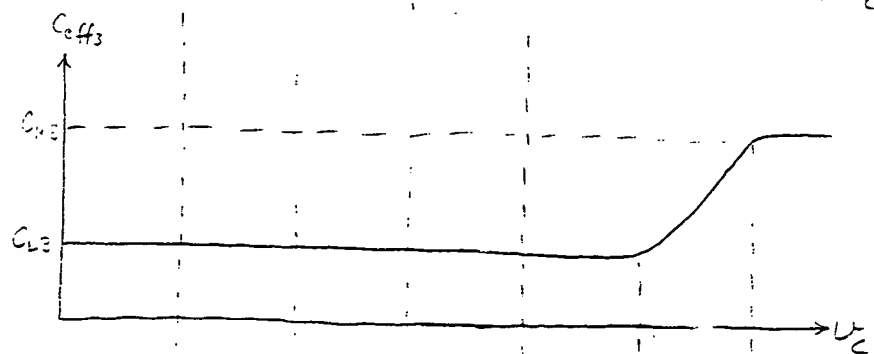
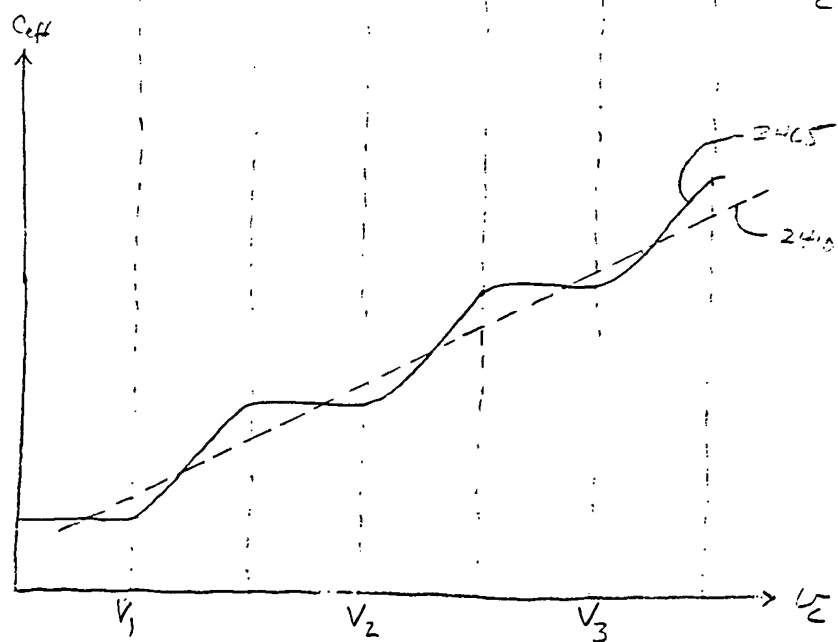
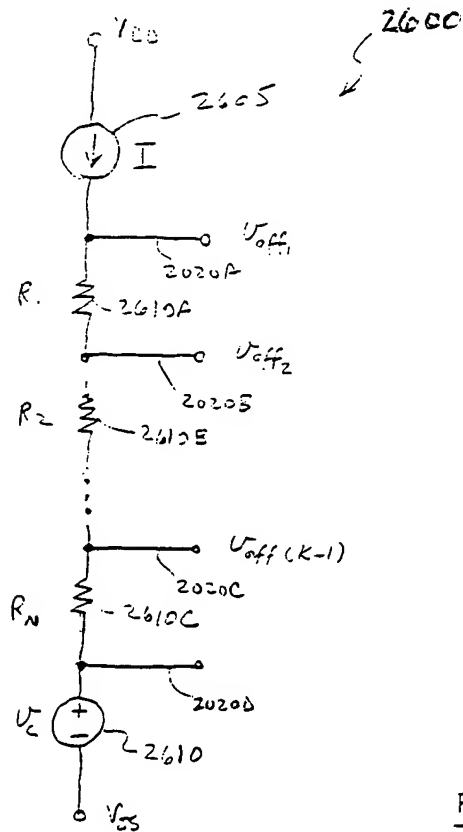
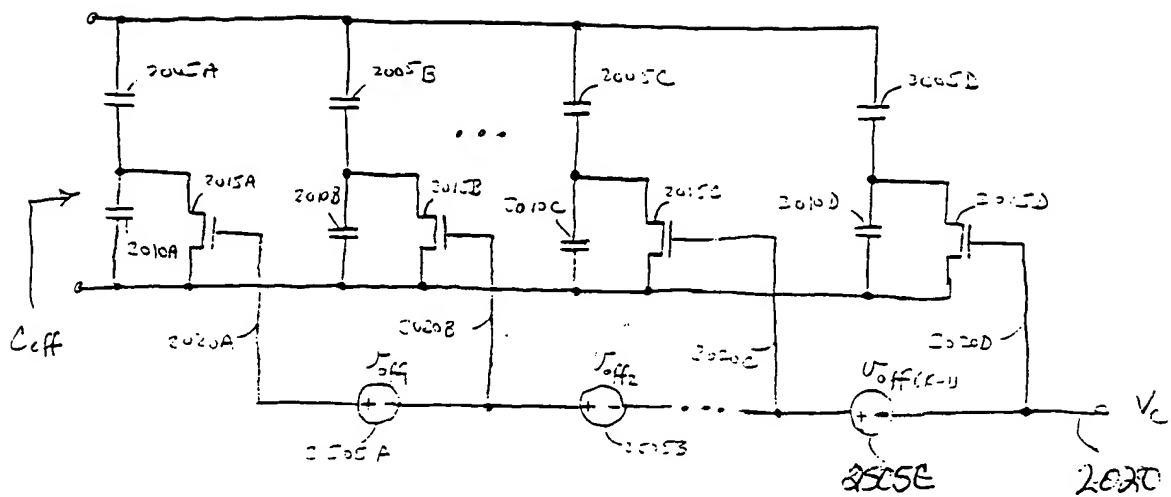


FIG. 24D





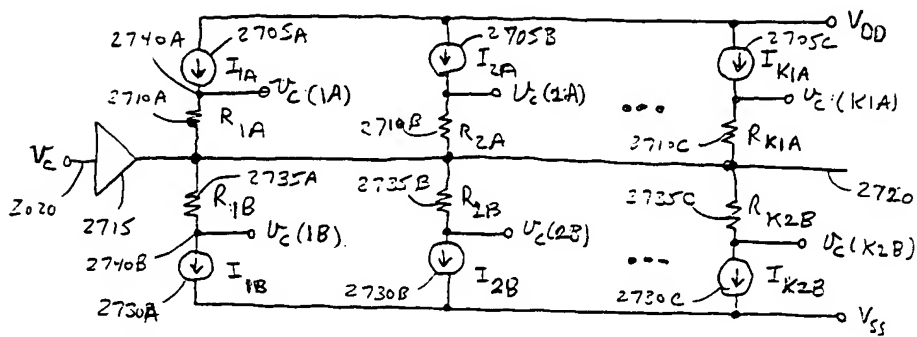


FIG. 27

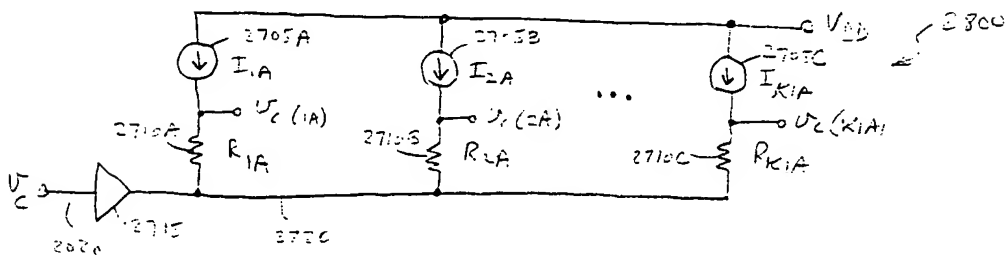


FIG. 28

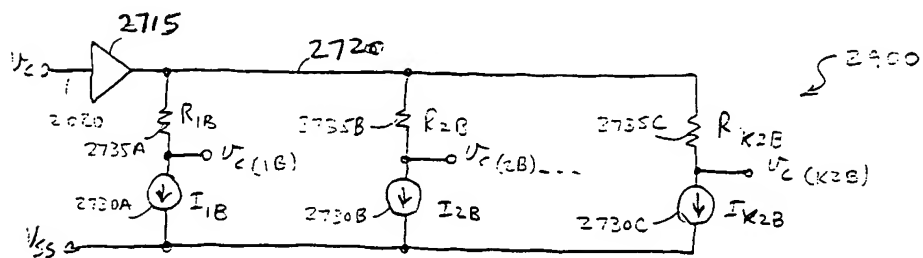


FIG. 29

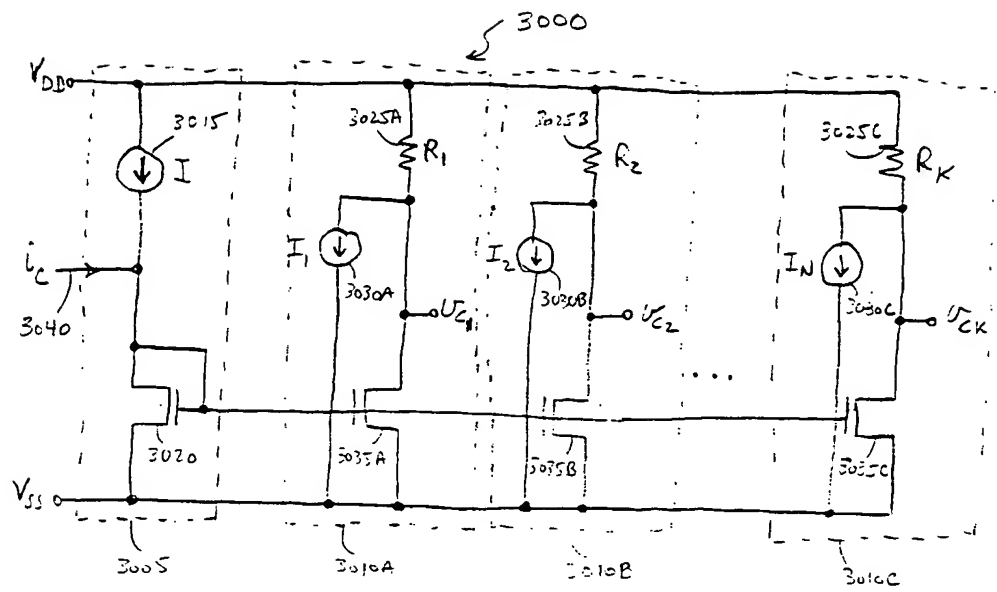


FIG. 30



3100A

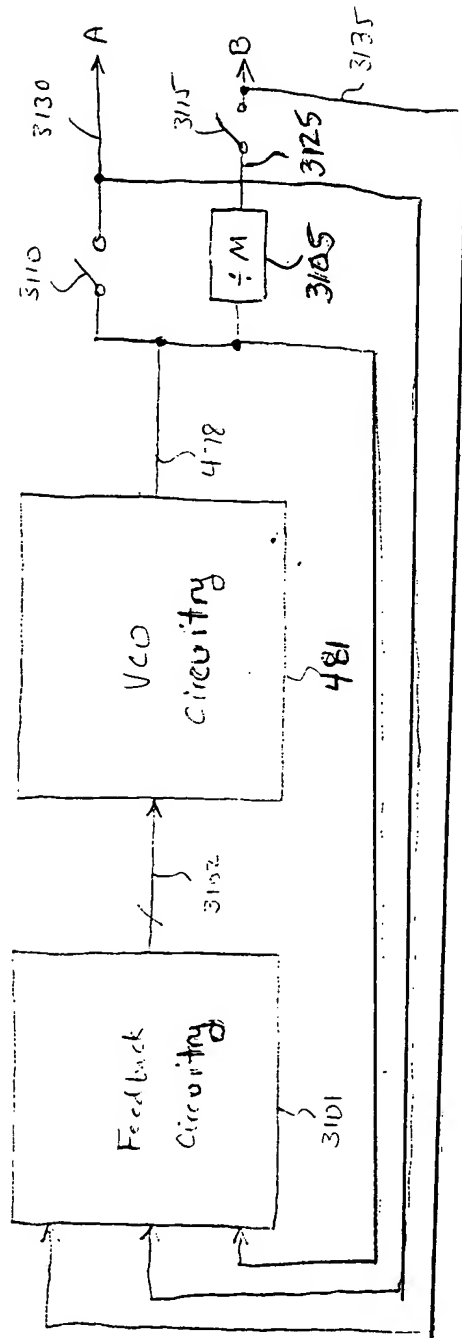
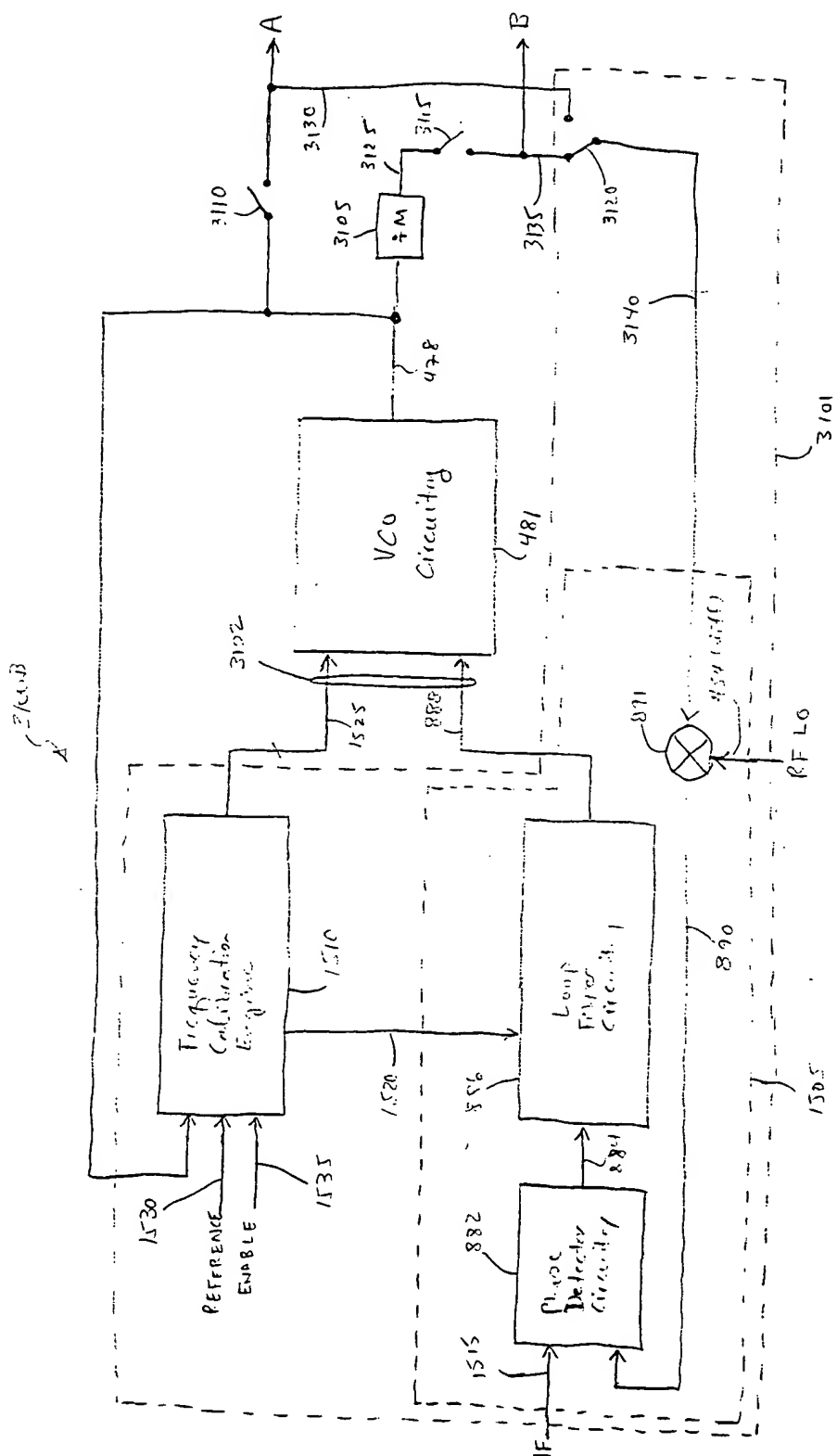


FIG. 31A



213.213

FIG. 32

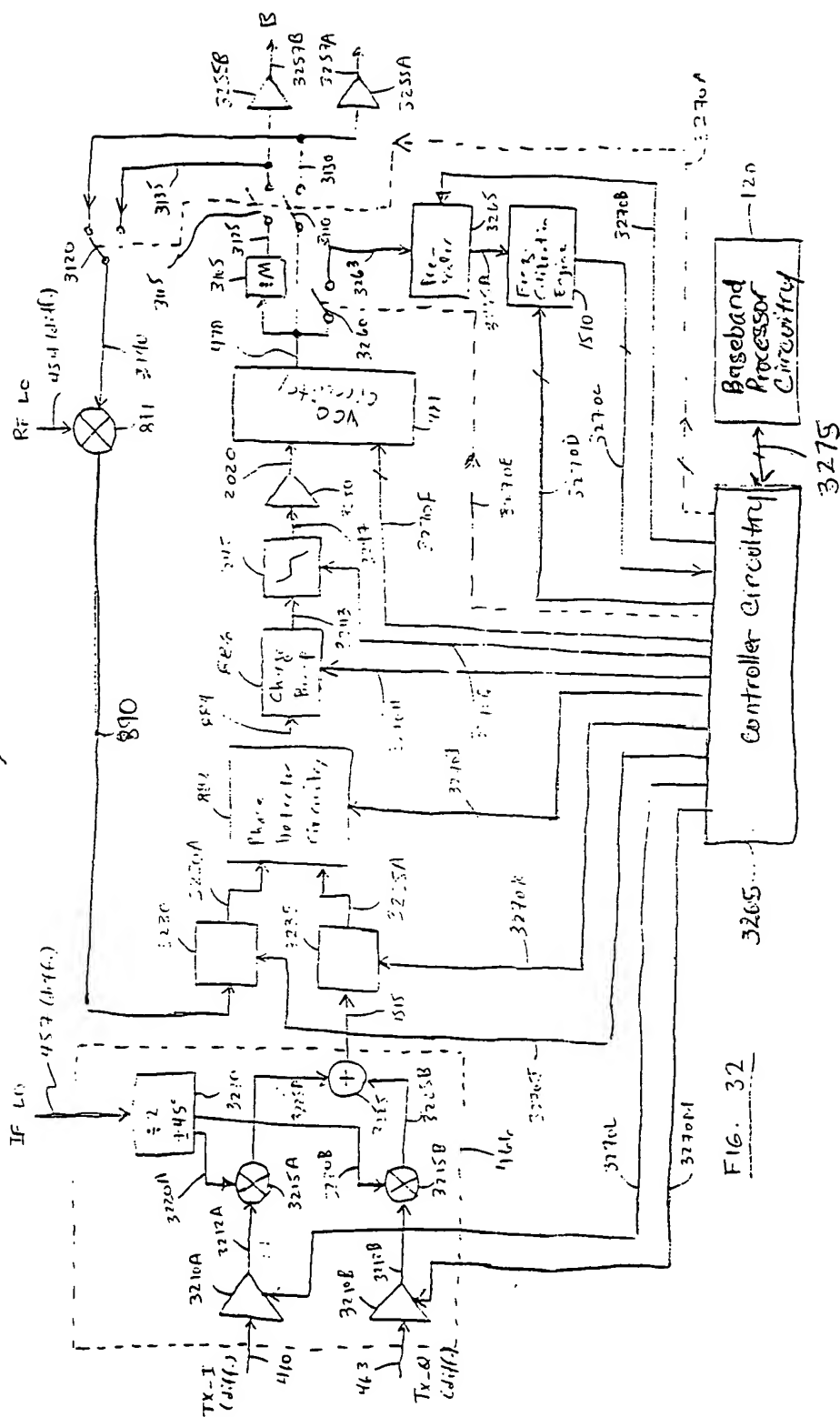


FIG. 32

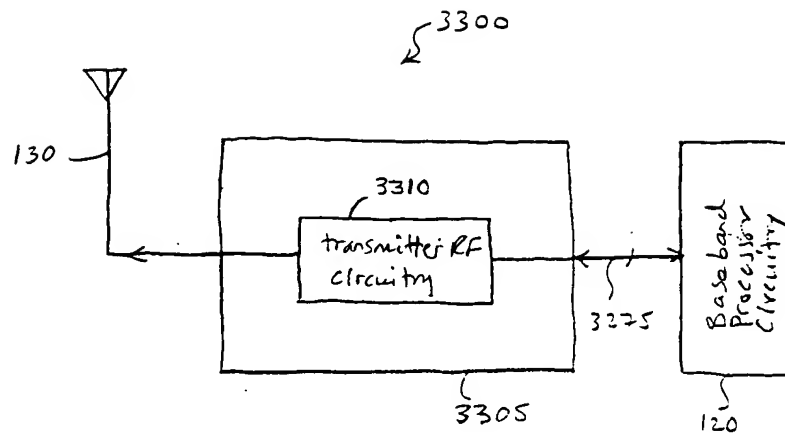


FIG. 33

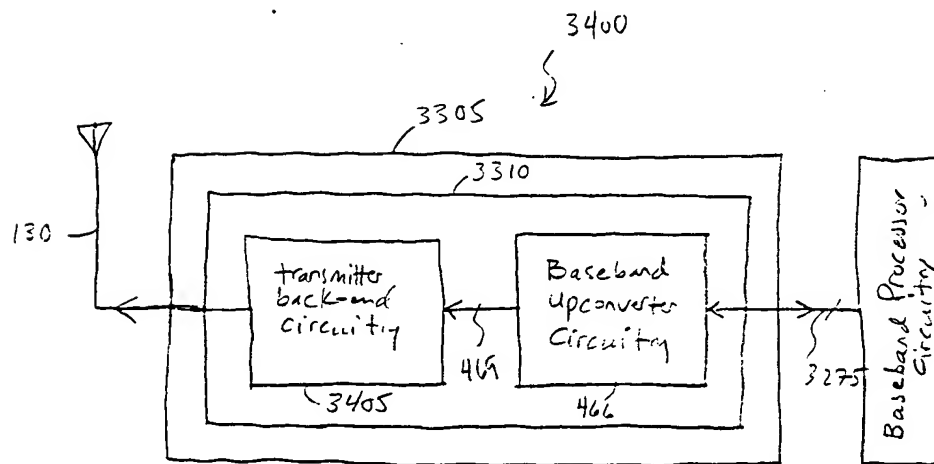


FIG. 34

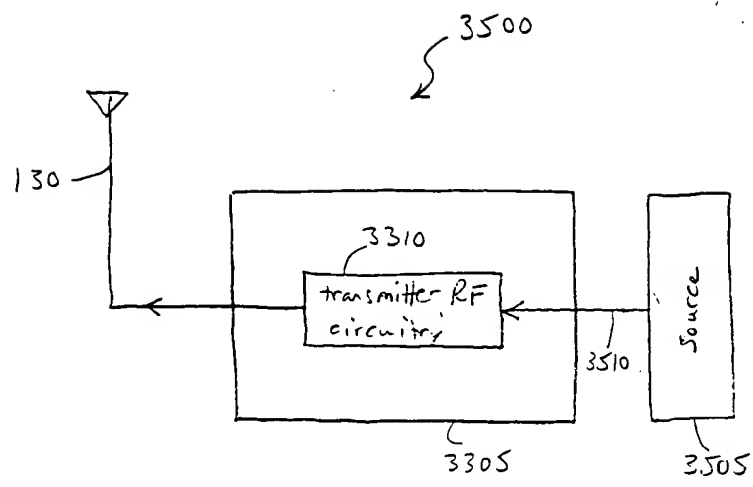


FIG. 35

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